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July 15, 2008

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Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Re: U.S. Patent No. 7,148,959

Issued: December 12, 2006

For: Test Pattern, Inspection Method, and Device Manufacturing Method Patentees: DUSA et al.

Our Ref: 1857.7910000

Sir:

Transmitted herewith for appropriate action are the following documents:

- 1. Power of Attorney to Prosecute Applications Before the USPTO; and
- 2. Statement Under 37 C.F.R. 3.73(b).

The above-listed documents are filed electronically through EFS-Web.

In the event that extensions of time are necessary to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

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JDE/awt Enclosures

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